



Examiner: Jennifer T. Nguyen Group Art Unit: 2674 By

In re PATENT APPLICATION of:

Applicants : Jui-Hung HSU

Applic. No. : 09/909,854

Filed : July 23, 2001

For : COLOR THIN FILM TRANSISTOR

DISPLAY MODULE WITH SHIELDING-EFFECT CABLE

Attorney Ref. : SUND 213

April 5, 2005

INFORMATION

DISCLOSURE

STATEMENT

Mail Stop: Issue Fee Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

Sir:

This is an information disclosure statement submitted in compliance with the timing requirements of 37 C.F.R. § 1.97(d).

Attached are copies of two publications cited by the Japanese Patent Office in connection with a counterpart application in Japan. The publications are listed on the attached Form PTO-1449. It is certified that the enclosed references first became known by way of a communication from the Japanese Patent Office that was mailed on February 22, 2005, i.e., less than three months ago. The relevance of the references may be gleaned from the English language abstracts.

The requisite fee of \$180 is being submitted concurrently. Consideration of the submitted documents is respectfully requested.

ENCLOSED:\$

PGA/rw

Respectfully submitted,

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FORM PTO-1449				Atty. Docket		Application	Application No.		
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